interface intf(input clk, rst);

logic [3:0] coin;

logic sel;

logic item;

logic [3:0] change;

clocking drv\_cb@(posedge clk);

output sel;

output coin;

input item;

input change;

endclocking

clocking mon\_cb@(posedge clk);

input sel;

input coin;

input item;

input change;

endclocking

endinterface